

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Hideo NAMIKI  
Title: SYNCHRONOUS DATA  
TRANSMISSION SYSTEM  
Appl. No.: Unassigned  
Filing Date: August 2, 2001  
Examiner: Unassigned  
Art Unit: Unassigned

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to Examination of the above-identified application, Applicants respectfully request that the following amendments be entered into the application.

**In the Claims:**

In accordance with 37 C.F.F. § 1.121, please substitute for original claims 3-5 the following rewritten versions of the same claims, as amended. The changes are shown explicitly in the attached **"Version With Markings to Show Changes Made"**.

3. (Amended) The synchronous data transmission system according to claim 1, which further comprises a clock synchronizing means for synchronizing clocks generated in the sampling clock generators in the first and second terminals.

4. (Amended) The synchronous data transmission system according to claim 1, which further comprises a frequency difference eliminating means for eliminating the frequency difference between the clocks generated in the sampling clock generators in the first and second terminals.

5. (Amended) The synchronous data transmission system according to claim 1, wherein a synchronous data transmission line is connected to the data generators and the data reproducers in the first and second terminals.

Please add the following new claims:

11. (New) The synchronous data transmission system according to claim 2, which further comprises a clock synchronizing means for synchronizing clocks generated in the sampling clock generators in the first and second terminals.

12. (New) The synchronous data transmission system according to claim 2, which further comprises a frequency difference eliminating means for eliminating the frequency difference between the clocks generated in the sampling clock generators in the first and second terminals.

13. (New) The synchronous data transmission system according to claim 2, wherein a synchronous data transmission line is connected to the data generators and the data reproducers in the first and second terminals.

**REMARKS**

Applicants respectfully request that the foregoing amendments to Claim 3-5 and the addition of new claims 11-13 be entered in order to avoid this application incurring a surcharge for the presence of one or more multiple dependent claims.

Respectfully submitted,

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**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

3. (Amended) The synchronous data transmission system according to claim 1 **[or 2]**, **[wherein]** which further comprises a clock synchronizing means for synchronizing clocks generated in the sampling clock generators in the first and second terminals.

4. (Amended) The synchronous data transmission system according to claim 1 **[or 2]**, which further comprises a frequency difference eliminating means for eliminating the frequency difference between the clocks generated in the sampling clock generators in the first and second terminals.

5. (Amended) The synchronous data transmission system according to claim 1 **[or 2]**, wherein a synchronous data transmission line is connected to the data generators and the data reproducers in the first and second terminals.